



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE NOTICE OF APPEAL FROM THE PRIMARY EXAMINER TO THE BOARD OF PATENT APPEALS AND INTERFERENCES

In re application of:

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KIAN	TENG 1	ENG ET	AL.		
Serial 1	No.: 09/	766,477	(TI-22944.2)		
Filed: .	January	19, 200	l		
For: V	ERTICA	AL BAL	L GRID ARRAY INTEGRATEI	CIRCUIT PACKAGE	
Group	Art Uni	t: 2827			
Examir	ner Jame	es M. M	tchell		
P.O. Bo	issioner ox 1450	for Pate A 22313			
SIT:					
			·	als from the decision dated April 21, 2006 of the Primary Examine	
finally	rejecting	g claims	21-25, 27, 28 and 37, all of the re	ejected claims.	
	The ite	em(s) ch	ecked below are appropriate:		
1.	_	An ext	n extension of time to respond to the final rejection		
		_	was granted on		
			is requested for month((s).	
2.	_	A timely response to the final rejection has been filed, as provided in 841 O.G. 1411.			
3.	_	Fee \$5	00.00:		
			Not required (Fee paid in prior	appeal)	
		<u>_xx</u> _	The Commissioner is hereby a overpayment to Account No. 20	uthorized to charge any fees which may be required, or credit any -0668. A duplicate copy of this sheet is enclosed.	
Signature [Rule 191(b)] Post Office Address (to which correspondence is to be sent)				Jay M. Cantor, Reg. No. 19906 Texas Instruments Incorporated P. O. Box 655474, M/S 3999 Dallas, TX 75265 (301) 424-0355 (972) 917-5293	